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Docket No.: GR 97 P 1049 D

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By: 

Date: September 3, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernhard Schätzler et al.
Applic. No. : 09/688,465
Filed : October 16, 2000
Title : Electronic Component With an Integrated Circuit Mounted on an Island of a Lead Frame
Examiner : Nitin Parekh - Art Unit: 2811

RESPONSE

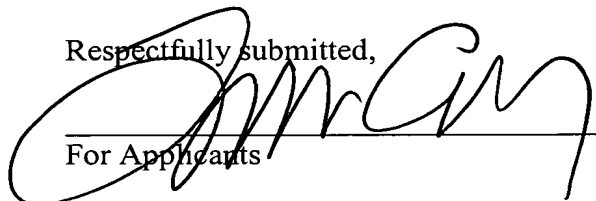
Hon. Commissioner for Patents,

Sir:

Responsive to the final Office action dated May 19, 2003, and supplemental to the *Response under 37 C.F.R. 1.116* submitted on August 19, 2003, applicants herewith submit an executed *Declaration under 37 C.F.R. 1.131* as well as a certified translation of German Published, Non-Prosecuted Patent Application No. DE 195 26 010 A1. A copy of said reference was submitted with the aforementioned response on August 19, 2003.

Applicants have therewith overcome the rejection under 35 U.S.C. 103(a) and the allowance of claims 1-5 is respectfully requested.

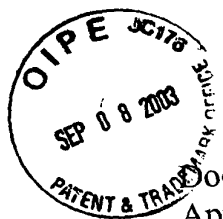
Respectfully submitted,


For Applicants

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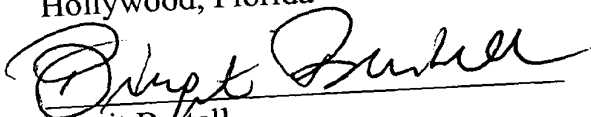
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Application No.: 09/688,465

CERTIFICATION

I, the below named translator, hereby declare that: my name and post office address are as stated below; that I am knowledgeable in the English and German languages, and that I believe that the attached text is a true and complete translation of German Published, Non-Prosecuted Patent Application No. DE 195 26 010 A1.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Hollywood, Florida


Birgit Bartell

September 3, 2003

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